Abstract of the Disclosure

    To obtain a correct vector address even if an
interrupt occurs during erasing or programming of the
data in a built-in ROM 18 by moving a part of a built-
in RAM13 to a vector address area by a bus controller
27. Thereby, a microcomputer is prevented from
running away and the safety of a system is improved at
the time of on-board programming of the built-in ROM
18.